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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10030739	FILING DATE 01/11/2002	CLASS 156	SUBCLASS 209	GAU 4734	EXAMINER Nathan Ha
**APPLICANTS: Ono Masahiro; Shiraiishi Tsukasa <i>2811</i>					
**CONTINUING DATA VERIFIED: This application is a 371 of PCT/JP01/03922 05/11/2001					
** FOREIGN APPLICATIONS VERIFIED: JAPAN 2000-140072 05/12/2000					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO 2002-0013A	
Verified and Acknowledged Examiners's initials					
TITLE : Semiconductor device, mounting circuit board, method of producing the same, and method of producing mounting structure using the same					
U.S. DEPT. OF COMM/PAT & TM-PTO-436L (Rev. 12-94)					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		Print Fig.	
<input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner	
PREPARED FOR ISSUE		WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.	

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